15:00 Welcome
15:05 **HUAWEI** Ing. Federico Vecchi, Principal Engineer
15.15 **LEONARDO** Dott.ssa Maria Rita Stoppa, HR, Ing. Alessandro Raggi, CTO, Div. Elettronica
15.25 **INFINEON TECHNOLOGIES ITALIA**
   Ing. Massimo Grasso, Director of Product Concept Engineering, Pavia
15.35 **ALLEGRO MicroSystems**
   Ing. Roberto Peritore, Director Design Engineering, Regulators & Lighting
15.45 **AMS** Ing. Marco Cerchi, Engineering Manager, Pavia
15.55 **ENI** Ing. Marco Nardini, Responsabile dell’unità Power quantitative analysis
16.05 **TDK-InvenSense** Ing. Serena Porrazzo, Staff Analog Design Engineer
16.15 **INVENTVM** Ing. Domenico Granozio, Application team Senior Manager, Pavia
16.25 **STMicroelectronics** Ing. Juri Giovannone, Senior Digital Designer
16.35 Q&A

Prof. Sabina Merlo, Referente Laurea Magistrale Electronic Engineering (LMEE)
Project Timeline

- May 26: LM+ Day
- July - September: Projects are published on the web
- 24 Sept-8 Oct: LM+ Calls and Applications
- 10-20 Oct: Interviews and selection
- 20-25 Oct: Matching Students with Companies
- March: Working Experience Starts (end Feb. 2023)